

REMARKS

Claims 15-21 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the above amendments and following remarks.

Turning now to the art rejections, Claims 15-21 were rejected under 35 U.S.C. § 102(b) as being anticipated by Thomsen or Chung.

It is respectfully submitted that Thomsen does not disclose or suggest the presently claimed invention including the method step of initializing a single buffer to act as a transmitter and receiver and performing a transmit buffering sequence and performing receive buffering sequences as defined in independent Claim 15.

The transmitting and receiving functions of Thomsen are achieved with two separate devices.

Chung does not disclose or suggest the presently claimed invention including initializing a single buffer to act as a transmitter and receiver and performing a transmit buffering sequence and performing receive buffering sequences.

Chung does not disclose a single buffer for both transmit and receive operations.

If Chung pointed structure were applied to a single transmit and receive buffer, the receive data and the transmit data would write over each other in the FIFO because there is no coordination mechanism between the transmit read write pointers and the receive read write pointers.

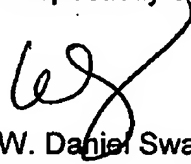
Claims 15-21 are patentably distinct over the applied reference.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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